

# A Six Legs Buck-boost Interleaved Converter for KERS Application

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**Abstract.** This paper addresses the design of a bi-directional DC/DC power converter to interface a supercapacitor bank and a motor-generator unit. The design is based on an interleaved six legs topology in which the current is shared among six inductors to minimize their weight and cost, allowing, besides, a low switching frequency to lessen switching losses. The converter is conceived to be employed in an electric Kinetic Energy Recovery System for Internal Combustion Engine Vehicles. The system makes use of a supercapacitor as a storage system, and a motor-generator unit connected to the drive shaft for vehicle acceleration and braking. The system uses available commercial devices, thus obtaining a cheap and high-efficiency conversion chain. It is shown how the design criteria differ from traditional interleaved converters. The same topology allows minimizing the input and output ripple and improving the reliability in case of fault as well. Losses are reduced both by sharing the currents and by a suitable control strategy, which allows more converters to be connected in parallel to increase the delivered power. Results, given in simulation, assess the stability and dynamic performance of the conversion circuit, showing a low current and voltage ripple in all operating conditions.

**Key words.** Hybrid Vehicle; Urban Driving Cycle; Kinetic Energy Recovery System; KERS; Supercapacitor; Vehicle Fuel economy; Regenerative Braking.

## Nomenclature

CCM	Continuous Conduction Mode
DLC	Double Layer Capacitor
ESRDC	Parasitic resistance of the supercapacitor
EUDC	Extra-Urban Cycle
EV	Electric Vehicle
HEV	Hybrid Electric Vehicles
ICEV	Internal Combustion Engine Vehicle
KERS	Kinetic Energy Recovery Systems
MGU	Motor Generating Unit
NEDC	New European Driving Cycle
PC	Power Converter
RBS	Regenerative Braking Systems
RGSE	Revised Generalized Steinmetz Equation
SC	supercapacitor
C	Output capacitor of the six-legs DC/DC converter
D	Duty cycle of the power switch of the six-legs DC/DC converter
$\Delta I_L$	Peal-to-Peak current variation in an inductor of the six-legs DC/DC converter
$\Delta I$	Peal-to-Peak input current of the six-legs DC/DC converter
$\Delta V_o$	Peal-to-Peak of the AC component of the output voltage of the six-legs DC/DC converter
$E_{acc}$	Energy required during acceleration phase
$E_{brak}$	Energy required during braking phase
fs	Switching frequency of a power switch of the six-legs DC/DC converter

$G(s)$	Closed loop transfer function of the six-legs DC/DC converter
$G_o(s)$	Transfer function in Laplace domain of the output voltage versus the duty cycle of the six-legs DC/DC converter
$G_{iL}(s)$	Transfer function in Laplace domain of the current through an inductor versus the duty cycle of the six-legs DC/DC converter
$\bar{I}$	Mean value of the current
ID	Current flowing through a diode of the six-legs DC/DC converter
$I_{DS}$	Rated current of a power switch of the six-legs DC/DC converter
$I_L$	Current flowing through an inductor of the six-legs DC/DC converter
$I_{rr}$	Reverse Recovery current of a diode of the six-legs DC/DC converter
KI	cancellation factor
$L_{CCM}$	Minimum value of the inductor to guarantee the CCM of the six-legs DC/DC converter
L	Inductor of the six-legs DC/DC converter
N	Number of legs of the six-legs DC/DC converter
$P_{conv}$	Rated power of the six-legs DC/DC converter
$P_c$	Power losses for a power switch of the six-legs DC/DC converter during conduction
$P_{c_c}$	Power losses on the output capacitor of the six-legs DC/DC converter
$P_{D_{cond}}$	Power losses for a diode of the six-legs DC/DC converter during conduction
$P_{D_{rr}}$	Power losses for a diode of the six-legs DC/DC converter due to reverse recovery
$PI_i(s)$	Proportional Integral regulator of the current of the six-legs DC/DC converter
$PI_v(s)$	Proportional Integral regulator of the voltage of the six-legs DC/DC converter
$R_{ds(ON)}$	Conduction resistance of a power switch of the six-legs DC/DC converter
$R_D$	Conduction resistance of a diode of the six-legs DC/DC converter
$P_{SW}$	Power losses for a power switch of the six-legs DC/DC converter during switching
$Resr$	Parasitic resistance of the output capacitor of the six-legs DC/DC converter
$R_L$	Parasitic resistance of an inductor of the six-legs DC/DC converter
$t_{on}$	Turn-on time of a power switch of the six-legs DC/DC converter
$t_{off}$	Turn-off time of a power switch of the six-legs DC/DC converter
$t_{rr}$	Reverse recovery time for a diode of the six-legs DC/DC converter
$V_o$	Output voltage diode of the six-legs DC/DC converter
$V_{DS}$	Maximum drain-source rated voltage of a power switch of the six-legs DC/DC converter
$V_{SC}$	Voltage across the terminal of the supercapacitor
$V_\gamma$	Threshold voltage of a diode of the six-legs DC/DC converter.

## 1. Introduction

The development of Sustainable mobility requires the exploitation of new solutions to reduce fuel consumption and to respect the environment. Among these, growing attention is addressed to road transport emission and urban pollution [1-2], advances in combustion optimization [3], and control [4] of alternative and cost-effective fuels, as well as the optimal management of vehicles drive-line [5-6]. Nevertheless, one of the heaviest lack of traditional internal combustion engine vehicles (ICEV) is the massive amount of energy lost by friction during the braking phases. This energy, suitably recovered, could be efficiently employed during acceleration, contributing to lower the energy consumption of the vehicles and the related pollution.

To face up with these issues, several Regenerative Braking Systems (RBS) or Kinetic Energy Recovery Systems (KERS) have been studied and optimized for different kinds of vehicles (Electric, Hybrid, or Internal combustion engine vehicle). They can be equipped with an energy storage system of different type (mechanical, electrical, chemical, hydraulic), and suitable or not for retrofit application on current production vehicles. In Hybrid Electric Vehicles (HEV), as well as in pure Electric Vehicles (EV), the presence of power generator and/or motor interfaced to high capacity energy storage allows an easy implementation of regenerative braking, using fuel cells, battery, or supercapacitors (SC) as storage systems.

Recently, the so-called starter-generator systems have been considered for vehicle powering in starting and generating functions. Their growth, both in rated power and control complexity, could improve the hybridization on all vehicles thanks to the reduction of the cost of power electronics and other related technologies [7]. Lately, the development of advanced storage systems as supercapacitors has strengthened to manage the energy exchange in extreme braking conditions [8, 9, 10]. Usually, SCs are used together with traditional battery [11,12], where they are strategic to manage the energy storage by buffering the battery during power transients and enabling more significant acceleration and regenerative braking capabilities [13]. The voltage adaptation between the starter/generator and/or the battery is performed by suitable power converters to guarantee a bidirectional flow of energy and high conversion efficiency [14,15]. Differently from the papers mentioned above, the proposed approach considers only SCs as a storage system to exchange energy with the starter-generator. It aims to obtain a cheaper plug-in system for large scale internal combustion engine vehicles in urban transportation.

The use of supercapacitors as a fast and efficient energy storage solution in power applications is widely recognized. In fact, they offer higher power densities to traditional batteries and energy densities from 10 to 20 times higher than electrolytic capacitors. The so-called Double-Layer-Capacitors (DLCs) are available on the market with capacitance values up to 1500 F; even if the voltage of a single unit is low (about 2.7 V) higher rated voltages can be obtained by suitable series-parallel arrangements [16]. One of the major drawbacks of this technology consists of its low volumetric and gravimetric energy density in comparison with batteries or fuel cells.

However, DLCs become an interesting option when highly dynamic charging or discharging profiles are concerned, with high current rates [16]. This is justified by their relevant high power capabilities (specific power densities up to about 3400 W/kg and specific energy up to 30 Wh/kg, and life cycles of up to  $10^6$ ) [17].

Unlike electrified vehicles, Internal Combustion Engine Vehicles (ICEV) are not equipped with a generator, motor, and batteries of adequate power and capacity to perform regenerative braking. These kinds of vehicles, hence, mechanical, hydraulic, or pneumatic energy storage devices [18] have been proposed to recover the vehicle kinetic energy during braking phases. The comparison among regenerative braking systems, proposed in [19], underlined that the electric energy recovery systems suffer both of losses of energy due to energy transformations and long recharging times. On the other hand, Hydraulic/Pneumatic based solutions exhibit a limited energy storage, and for flywheels based systems, a loss of rotational energy over time due to friction and air resistance is noticeable. The proposed approach aims to improve the efficiency of the power conversion chain in electric KERS by a suitable design of the DC/DC converter and to adopt supercapacitors as storage units. In particular, the system presented in this paper is intended for the application to ICEV. It is composed of a SC interfaced to a motor-generator unit (MGU) through a power converter, whose function is to adapt the voltage levels between SC and MGU during operation. A feasibility study of the system has been already proposed in [20], the theoretical analysis in [21], and a simulation of the whole system in [22]. This paper analyses the design of a Buck-boost power converter to manage the electric power energy exchange between the SC and the MGU. It is shown that the efficiency of the DC/DC converter plays a key role since the power is processed twice. Its main features are the modularity and efficiency achieved, minimizing the cost of the hardware.

## 2. The KERS Operating Principle

The Kinetic Energy Recovery System (KERS) considered in this work is composed of a supercapacitor, as the energy storage of the system. It is electrically interfaced, through the power converter (PC) developed in this paper, to the motor-generator unit (MGU). It is mechanically connected to the drive shaft and then to the wheels via a fixed gear ratio. During braking, the MGU converts the kinetic energy of the vehicle into electric energy; the power converter has to operate the necessary conversion of voltage and current to transfer this energy to the SC. On the contrary, during acceleration, the SC delivers energy to the power converter that supplies the MGU, which contributes to vehicle propulsion through the drive shaft. The whole system is bidirectional, thus allowing the mechanical energy to be converted into electrical energy (braking) and vice versa (acceleration). The power converter comprises two stages: a DC/DC and a DC/AC. This paper is focused on the design of the DC/DC stage. It is connected to the SC and to the DC/AC inverter of the MGU. The SC voltage is lower than the voltage at the DC/AC stage. For this reason, the DC/DC converter will be operated in boost mode (increasing the voltage of the SC to supply the inverter) during

acceleration. Instead, during braking, it will be operated in step-down mode (lowering the voltage of the DC/AC to charge the SC).

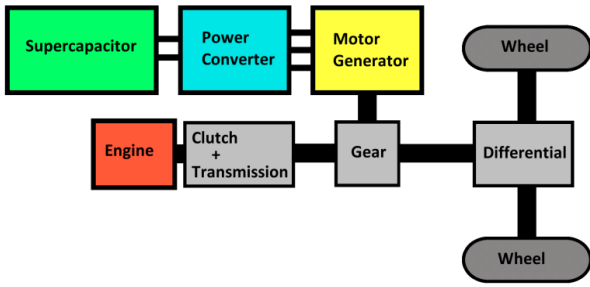


Fig. 1. Schematic representation of the KERS

### 3. DC/DC Converter Constraints Design

The voltage of both the storage system and of the MGU are two constraints for the DC/DC converter together with the rated power. They depend on the vehicle and its performance during braking and acceleration.

#### 3.1. The Supercapacitor Storage System

The Supercapacitor based storage system has been chosen based on of the New European Driving Cycle (NEDC), it consists of four repetitions of the urban cycle ECE-15 and one of extra-urban cycle EUDC [23].

The energy delivered from the SC bank to the MGU has been calculated in the worst case for a compact car whose mass is equal to about 980 kg. It corresponds to a peak of 10.3 kW reached at the end of the acceleration taken during the cycle ECE-15 as shown in figure 2.

$$E_{acc} = \int P dt = 32.9 Wh \quad (1)$$

The energy received during braking can be calculated in the same way. From figure 2 the maximum power reaches 6 kW and the energy to be stored is given by:

$$E_{brak} = \int P dt = 3.33 Wh \quad (2)$$

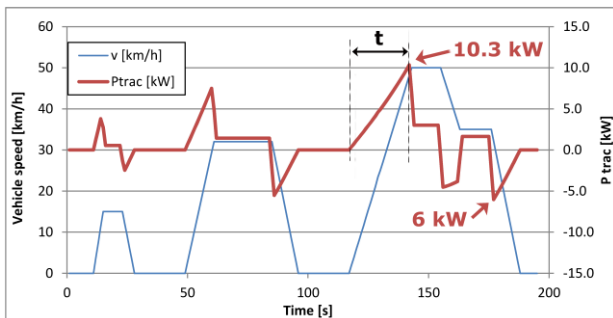


Fig. 2. Speed and power for the urban vehicle under study

On this basis the Maxwell SC Unit BMOD0083 P048 has been chosen. Its main features are:

- Rated Capacitance: 83 F
- Maximum ESRDC, initial: 10 mΩ
- Rated Voltage: 48 V
- Absolute Maximum Voltage: 51 V
- Absolute Maximum Current: 1150 A

This SC unit can deliver 26.5 Wh. This value is slightly lower than that calculated with (1); it comes from a trade-off: the higher capacity of the supercapacitor bank would improve performance but increasing the weight and cost. [21, 22].

#### 3.2 The Motor Generator Unit

The MGU has to guarantee the conversion from mechanical to electric energy and vice versa. It has been selected considering the peak of power. The MGU Motenergy ME0201013601 has been chosen. This is a 3-phase, Y-connected Permanent Magnet Synchronous Motor with an axial air gap. It exhibits an efficiency equal to 92% at DC voltage from 12V to 96V and a continuous current of 60A. The maximum rotor speed is equal to 5000 rpm. The continuous output power is 5 kW at 96 VDC at 4400 rpm and can deliver a peak output of 13 kW for 1 minute. This feature is compatible with the duration of the acceleration phase shown in figure 3. This kind of motor allows the voltage at the output of the DC/DC converter to be fixed to 98V. This slight increase, compared to the maximum rated voltage, compensates the voltage drop at high currents due to parasitic resistance of electric contacts. It can be noted that as much higher is the operating voltage as lower will be the current for given power so to lessen ohmic losses.

### 4. Design of the DC/DC converter

As far as the topology is concerned, the interleaved structure has been selected since it offers good efficiency and a reduced ripple on the current. Moreover, it assures high reliability since it can be operated with a reduced number of legs in case of failure. In particular, a six legs structure has been studied, as shown in Figure 3.

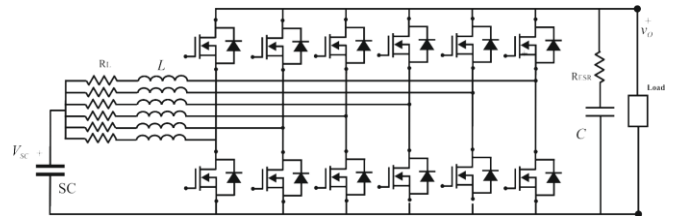


Fig. 3. Electric scheme of the six-legs interleaved converter.

This converter behaves as a Buck or a Boost depending on the devices operated. As an example, the boost mode is obtained for each leg by switching the bottom Mosfet and top with the top Mosfet idle, as shown in figure 4.

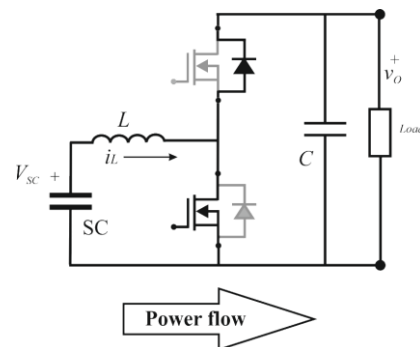


Fig. 4. Electric scheme of one leg of the interleaved DC/DC converter in boost operation.

The inductor has been designed considering a switching frequency  $f_s=20\text{kHz}$  to assure low switching losses in the power devices, and a maximum ripple of the current of 3A peak-to-peak. Despite the low value of the switching frequency the fundamental harmonic of the output ripple shows a frequency of 120kHz, and the total output ripple is reduced by the operation of six legs, as it will be shown in the following. The design is performed to maintain the converter operating in Continuous Conduction Mode (CCM) so that the current flows through the inductor without interruption during the switching period. The threshold value of inductor to assure the CCM is calculated in the worst case; it is given by:

$$L_{CCM} = \frac{V_o(1-D)D}{\Delta I_L f_s} = 408\mu\text{H} \quad (3)$$

Where  $f_s$  is the switching frequency,  $V_o$  is the output voltage, and  $\Delta I_L$  is the maximum peak-to-peak variation of the input current. The current flowing through the inductor contains a DC value and a triangular-shaped AC component superimposed. To assure the CCM operation, the DC value must be higher than  $\Delta I_L/2$ . A 500μH inductor is chosen considering a tolerance up to 20%. It is reasonable to assume a maximum current of 10 A to maintain the cost as low as possible, hence a DC current of 8.5 A is admitted; on this basis, the rated power of the six-leg converter corresponds to about 2400W. Higher power can be obtained employing more converters in parallel, as it will be shown later.

The elements that lead to a reduction of the overall cost of the converter are: a) the use of more inductors with a lower rated current, b) the minimization of the input and output filter allowed by the increased frequency of the input and output ripple, c) the reduction of cooling system due to the improved efficiency especially at light load. On the other hand, the converter requires more power devices and gate driver circuitry and a more sophisticated control algorithm; anyway, this complication does not give a significant increase to the cost, as explained in [24]. From now on, the analysis of a single module with 2400W of rated power will be carried out. The design constraints of the converter are summarized in table 1.

**Table 1.** Constraints Design of the DC/DC converter.

Parameter	Symbol	Value
Input voltage	$V_{SC}$	48 V
Output voltage	$V_o$	96 V
Number of legs	N	6
Rated power	$P_{conv}$	2400 W
Maximum ripple of the current in a leg	$\Delta I_L$	3 A
Maximum current in a leg	$I_L$	10 A
Switching frequency	$f_s$	20 kHz

The output capacitor is designed considering  $D \approx 1$  as worst case and an output voltage ripple  $\Delta V_o$  of 3%. It results:

$$C = \frac{D}{R f_s (\Delta V_o/V_o)} = 416\mu\text{F} \quad (4)$$

The commercial value of 560μF is chosen, allowing a ripple of about 2.2%, considering a load resistance of 4Ω as the worst case. As far as the power switches are concerned, a CoolMOS type with 25 A of rated current has been selected. The list of commercial devices, including parasitic components, is given in table 2.

**Table 2.** Commercial components of the six legs interleaved converter.

Symbol	Rated value	Supplier	Code
L	$I_L = 10 \text{ A}$ , $L = 500 \mu\text{H}$ , $R_L = 50 \text{ m}\Omega$	Vishay	IHV15BZ500
C	560 μF, Resr = 160 mΩ	Epcos	B43511A 4567M007
MOSFET	$V_{DSS} = 650 \text{ V}$ , $R_{DS(on)} = 110 \text{ m}\Omega@25^\circ\text{C}$ , $I_{DS} = 25 \text{ A}$	Infineon Techn.	IPA60R125CP CoolMOS

## 5. Efficiency evaluation

The efficiency has been calculated by evaluating the losses versus the current in each device. The losses depend in different way on the current, they can be constant, linear quadratic with the current, hence it is expected that the efficiency curve will exhibit a maximum [24]; unfortunately during KERS operation, the power transferred between SC and MGU varies appreciably; for this reason, a proper control strategy has been conceived to maximize the converter efficiency apart from the power involved.

The following losses have been considered: a) switching losses on Mosfet devices, b) conduction losses on Mosfet devices, c) conduction losses on a diode, d) diode recovery losses on a diode, e) Joule losses on the parasitic resistance of the inductor and magnetic losses, f) magnetic losses, g) Joule losses on the parasitic resistance of the output capacitor.

The losses have been firstly evaluated for a unique leg operated in the worst case meaning with rated current and  $D=0.5$ . The switching losses for a single Mosfet device at rated current of  $I=10\text{A}$  and  $V=98 \text{ V}$  are:

$$P_{SW} = \frac{1}{2} V I (t_{on} + t_{off}) f_s \cong 0.1 \text{ W} \quad (5)$$

Where for the selected Mosfet  $t_{on}=t_{off}=5\text{ns}$ . The conduction losses on a Mosfet are:

$$P_c = D \left[ R_{ds(ON)} \left( \bar{I}_L^2 + \frac{\Delta I_L^2}{12} \right) \right] = 7.3 \text{ W} \quad (6)$$

Where the root mean square of the current has been calculated as the sum of the maximum DC value equal to 8.5A and the AC components which is a triangular waveform with  $\Delta I_L=3\text{A}$ .

The power loss on the diode are calculated considering the conduction and the recovery losses:

$$P_{D\_cond} = D \left[ V_\gamma \bar{I}_D + R_D \left( \bar{I}_D^2 + \frac{\Delta I_D^2}{12} \right) \right] = 0.05 \text{ W} \quad (7)$$

$$P_{D\_rr} = \frac{1}{2} V t_{rr} I_{rr} f_s = 17.3 \text{ W} \quad (8)$$

Where  $t_{rr} = 430$  ns,  $I_{rr} = 42$  A for the considered diode integrated into the Mosfet. The power loss on one inductor can be calculated on the basis of the square of rms value of the current multiplied for the parasitic resistance.

$$P_L = R_L \left( \bar{I}_D^2 + \frac{\Delta I_L^2}{12} \right) = 3.65 W \quad (9)$$

The magnetic losses have been calculated by the Revised Generalized Steinmetz Equation (RGSE) following the methods explained in [25] obtaining 0.2W for each core. The power loss on output capacitor in the worst case is given by:

$$P_{C\_C} = R_{ESR} (1-D) I_{rms}^2 = R_{ESR} (1-D) \left( \frac{\Delta I_L}{2\sqrt{3}} \right)^2 = 0.06 W \quad (10)$$

Where  $I_{rms}$  is the root mean square value of the AC current through the capacitor, it is equal to 0.87A.

**Table 3.** Power losses and efficiency calculation at rated power.

Loss	Value [W]
Mosfet switching losses (x6)	0.6
Mosfet conduction losses (x6)	43.8
Diode conduction losses(x6)	0.3
Diode reverse recovery losses (x6)	104
Inductor joule losses (x6)	21.9
Inductor magnetic losses (x6)	1.2
Capacitor losses	0.06
<b>TOTAL LOSSES</b>	<b>171.86</b>

At the rated power of 2400 W, the theoretical efficiency results equal to 92.8%. The equations (5-8) have been implemented versus the output current; then, the converter efficiency has been analyzed considering the operation of two, three, four, five, and six legs with the output current as a parameter. Each leg is operated so that the modulating carriers are phase-shifted of  $2\pi/N$ , where N is the number of operated legs. In this way, the ripple in the input and output current will be reduced as well as the current through the output capacitor, thus minimizing the losses on  $R_{esr}$ . The frequency of the ripple results higher (equal to 120kHz when all legs are operated), making easier the harmonic suppression.

The ripple on the input current depends on the duty cycle and on the number of operated legs. The analysis of the reduction of the current ripple in interleaved operation mode can be carried out by calculating the cancellation factor KI as the ratio between the input ripple  $\Delta I$  and the current ripple of each inductor  $\Delta I_L$  as in (10) for  $N=2,3,4,\dots$  [26]. This value is shown in figure 5, where it can be noted that, for example, when four legs are operated, the complete cancellation is obtained for  $D=0.25$  and  $D=0.75$ .

$$KI = \frac{\Delta I}{\Delta I_L} = N \frac{\prod_{i=1}^{N-1} \left| \frac{i}{N} - D \right|}{\prod_{i=1}^{N-1} \left( \left| \frac{i}{N} - D \right| + \frac{1}{N} \right)} \quad (1)$$

It can be noted that a three-leg converter could be sufficient to achieve a good ripple cancellation factor. Literature highlights that reduction is not significant for more than four legs, and the number of circuit components increases excessively; on the contrary, for the KERS purposes is appropriate that the number of the legs is augmented to six to adopt cheaper inductors and to improve the efficiency as explained hereinafter.

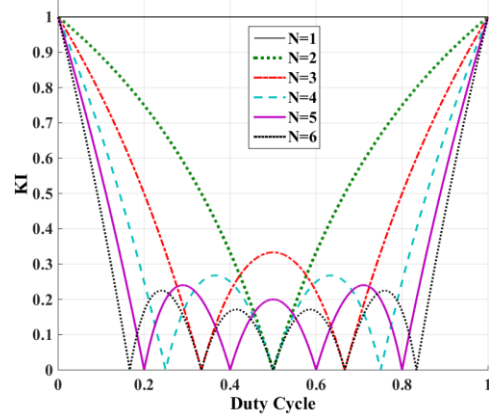


Fig. 5. Cancellation Factor versus duty cycle.

Moreover, this guarantees a redundancy in case of failure of a leg, thus also improving the reliability of the converter. Figure 6 shows the efficiency calculated considering a number of operated legs from one to six versus the input power.

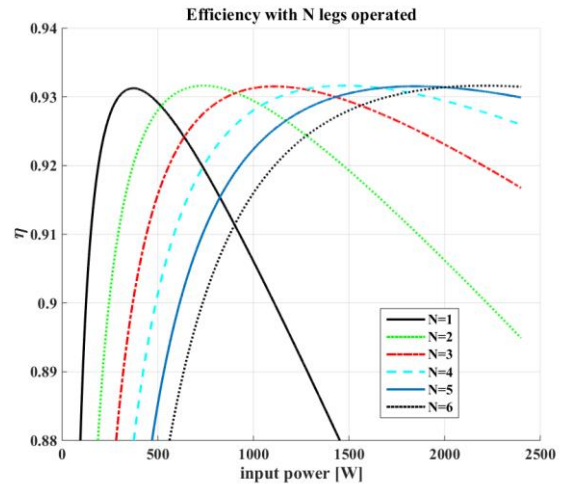


Fig. 6. Comparison of the efficiency for each group of legs operated.

It can be observed that each curve exhibits a maximum in different locations. In order to obtain a high and flat efficiency curve, the control strategy is designed to operate only the first leg up to input power of about 500W, two legs are operated from 500W up to about 900W and so on, until the maximum power is reached according to (12).

$$\text{operated legs : } \left\{ \begin{array}{ll} 0 < P \leq 516 & \text{leg } 1 \\ 516 < P \leq 906 & \text{legs } 1,2 \\ 906 < P \leq 1268 & \text{legs } 1,2,3 \\ 1268 < P \leq 1664 & \text{legs } 1,2,3,4 \\ 1664 < P \leq 2001 & \text{legs } 1,2,3,4,5 \\ 2001 < P \leq 2400 & \text{legs } 1,2,3,4,5,6 \end{array} \right. \quad (12)$$



This strategy allowed to obtain the whole efficiency curve represented in figure 7. It can be noted that the efficiency at rated power calculated by simulating the converter in real operating condition, shown in figure 6 for six legs operated, gives 93.2% confirming the theoretical worst case analysis shown in table 3.

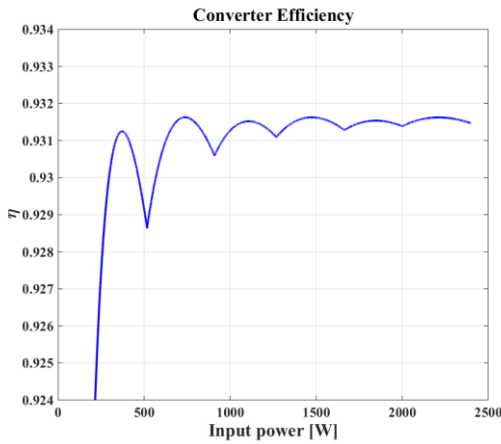


Fig. 7. Total efficiency of the interleaved converter.

### 6. Control Strategy

The control algorithm is based on two loops; a voltage loop to maintain the output voltage constant, and a current loop to force the current in each inductor. Based on the voltage error processed by a PI regulator, the reference current is obtained, it is compared with the current given by the converter, then another PI regulator gives the value of the duty cycle. The number of operated legs is defined by the reference current that depends on the power required or delivered by the SC bank. The current control, imposing the reference current in each inductor, avoids the circulation of parasitic currents. In addition, this feature allows more converters to be connected in parallel to increase the output power. The carrier signals are shifted in phase on the basis of the number of legs to be operated; in this way, the input and output ripple will be minimized, resulting in a frequency much higher than the switching frequency of each leg. The block diagram of the control system is sketched in Figure 8. The two transfer functions giving the voltage and the current versus the duty cycle are [27]:

$$G_v(s) = \frac{\tilde{v}_o(s)}{\tilde{d}(s)} = \frac{s \cdot (-R_L L i_L) + R V_{DC} (1-D) - R R_L i_L}{s^2 \cdot RLC + s \cdot (L + R R_L C) + R_L + R(1-D)^2} \tag{13}$$

$$G_{iL}(s) = \frac{\tilde{i}_L(s)}{\tilde{d}(s)} = \frac{s \cdot V_{DC} RC + R(1-D) i_L + V_{SC}}{s^2 \cdot RLC + s \cdot (L + R R_L C) + R_L + R(1-D)^2} \tag{14}$$

The two PI regulators have been properly designed, choosing

$$PI_i(s) = 101.8 \cdot [(1 + 0.00012/s)/s] \tag{15}$$

$$PI_v(s) = 742.8 \cdot [(1 + 0.0014/s)/s] \tag{16}$$

The closed loop transfer function obtained by (13-16) shows a dominant negative real part pole  $p_1 = -0.028 \text{ rad/s}$

and a couple of complex conjugate poles  $p_{2,3} = -0.2502 \pm j 1.3082$  giving an overdamped response of the output voltage.

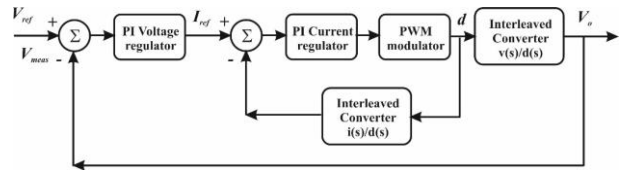


Figure 8. block diagram of the control system

### 7. Results

Simulation results have been carried out to verify the performance of the designed converter, i.e., the reduced ripple of the current, and the dynamic behavior. Figure 9 shows a steady-state test at rated power. Albeit the currents on the six inductors exhibit a period of 50 μs (corresponding to the switching frequency of 20 kHz), the current supplied by the supercapacitor shows a period of 8.33 μs (corresponding to a fundamental frequency of 120 kHz) and a ripple of 0.35%. It is much lower compared to the ripple on a single inductor. The load voltage experiences the same period and a ripple of 1.53%. Figure 10 shows a dynamic test obtained by varying the load from 4Ω to 12Ω at t=0.1s.

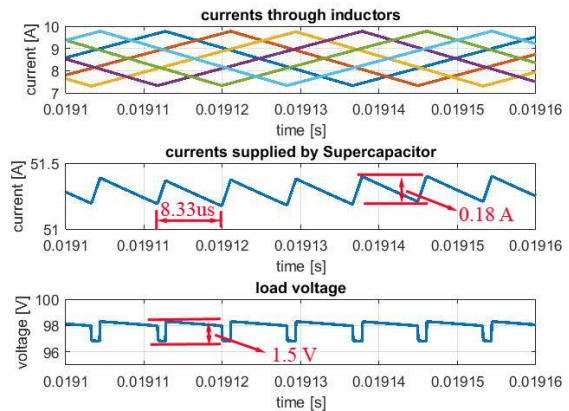


Fig. 9. steady state test at rated power: currents through inductors (top), current supplied by the supercapacitor (middle), load voltage (bottom).

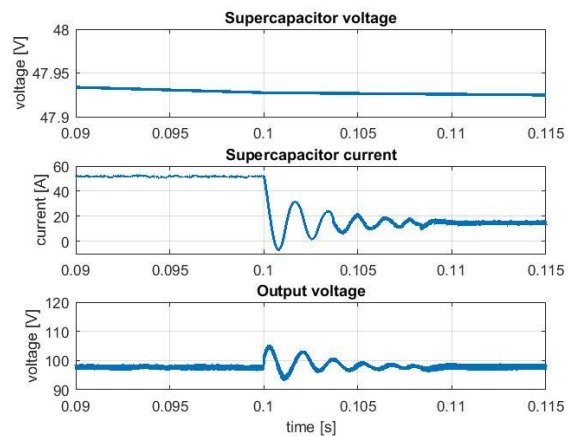


Fig. 10. dynamic test with load variation: supercapacitor voltage (top), current supplied by the supercapacitor (middle), load voltage (bottom).

It can be noted that, after the load reduction, the slope of the voltage of the supercapacitor is lessened as expected. The current supplied by the supercapacitor after a transient of about 10 ms reaches the new value, and the voltage on the load is maintained to 98 V by the control system. A test by varying the reference voltage from 80 V to 98 V at  $t=0.03$ s is sketched in figure 11. The output voltage requires only about 50 ms to reach the new value, and the overdamped transient confirms the calculation explained in section 6.

Both the load voltage and the supplied current exhibit a reduced ripple. In particular, the output voltage ripple is equal to 1.5 V peak-to-peak when the DC output is 98 V, and the ripple of the supplied current is equal to 0.35A peak-to-peak when the DC current is 33A. A final test is carried out to reproduce the current required for the 20s acceleration phase in which the power rises up to 10.3 kW; it is sketched in figure 3. Firstly, the operating condition of a single power module has been reproduced considering the power rising from zero to 2.4 kW in 4s corresponding to a single DC/DC converter unit. Figure 12 shows the power delivered to the MGU and the output voltage of the DC/DC converter; it can be noted that the voltage ripple remains low even near to the rated power. In figure 13, the current delivered by the SC and the voltage at the terminals of the SC are drawn. Also, in this case, a low current ripple can be appreciated.

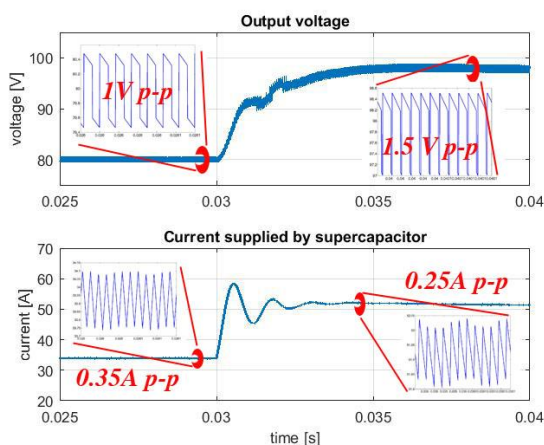


Figure 11. dynamic test with reference output voltage variation: output voltage (top), current supplied by the supercapacitor (bottom).

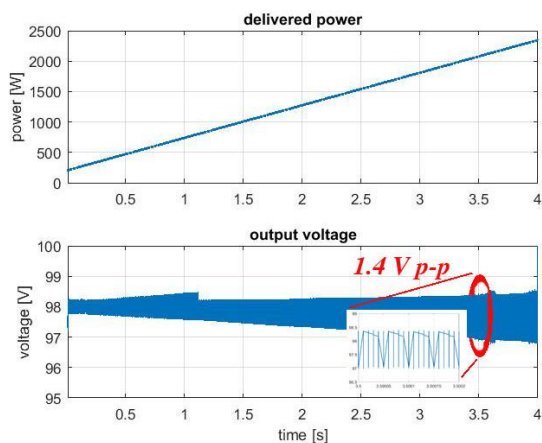


Figure 12. acceleration test: power delivered to the MGU (top), output voltage (bottom).

To satisfy the whole power requirements, five DC/DC power converters parallel connected have to be employed, as shown in figure 14. Each converter is supplied by the supercapacitor bank and delivers power to the DC/AC conversion unit of the MGU drive. Each converter is operated in a range of 4s to reach the maximum required power.

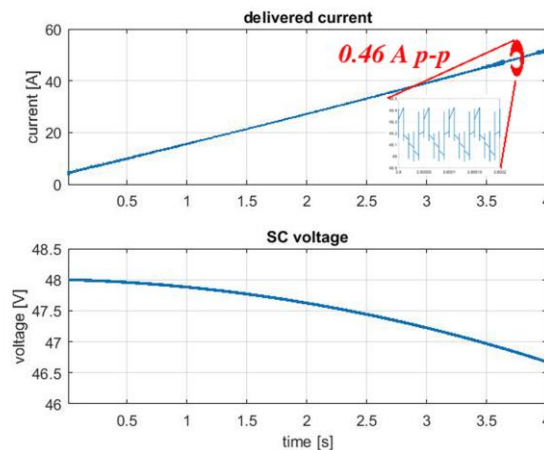


Figure 13. acceleration test: current delivered by the supercapacitor (top), supercapacitor voltage (bottom).

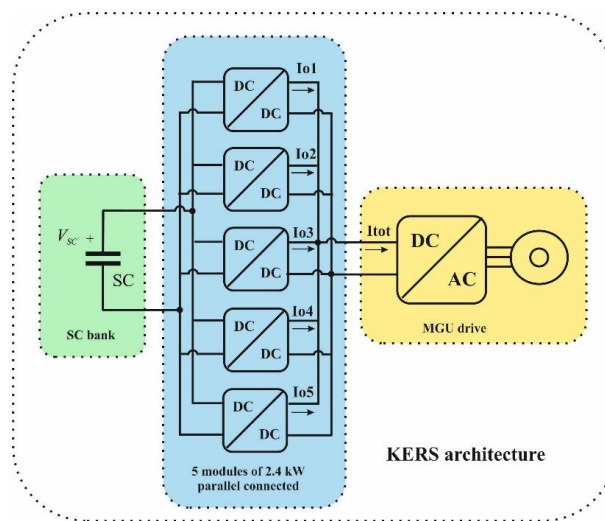


Figure 14. Schematics of the KERS architecture

In order to optimize the efficiency of the system, each converter is started when the previous unit is operated at rated power so that its efficiency is close to the maximum value. In a nutshell, only one converter starts at the beginning until its supplied power reaches the rated value of 2400W, then it continues to deliver a constant power of 2400W and the second converter starts in the same way and so on until the fifth converter supplies the rated power.

The waveforms of the current for each DC/DC converter and the total current delivered to the MGU drive are shown in figure 15. It can be noted that the control system is able to manage the power requirements so that when a converter reaches its nominal power, another converter starts operating.

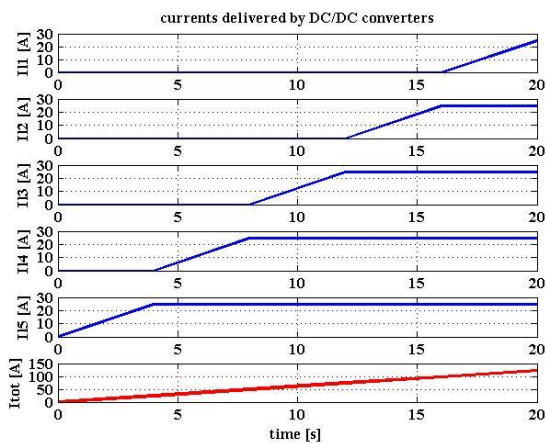


Figure 15. current delivered by DC/DC converters and total current during an acceleration.

## 8. Conclusions

A six legs interleaved converter has been designed to manage the power transfer between a supercapacitor and an motor-generator unit for an electric KERS proposed for internal combustion engine vehicles. The system is conceived to allow a significant saving of fuel, and consequent CO<sub>2</sub> reduction, in an urban area. The analysis has been performed based on commercial devices to obtain a cheap realization so that it can be used in low cost vehicles. Differently from a traditional interleaved converter, a greater number of legs has been chosen to guarantee higher efficiency and reliability. The number of legs assures a flat efficiency curve by a suitable choice of the legs simultaneously operated, according to the delivered power. The stability is assured by a control strategy based on voltage and current loops. Steady-state output current and voltage exhibit a very low ripple despite the low switching frequency; indeed, the fundamental harmonic of the output ripple is six times the switching frequency. Finally, the modularity of the proposed solution allows arbitrarily greater power to be managed.

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